4. **(ORIGINAL)** An apparatus as recited in claim 1 wherein the first device is an integrated circuit.

5. **(ORIGINAL)** An apparatus as recited in claim 1 wherein the first device is an integrated circuit disposed on a substrate, wherein the substrate is electrically coupled to the integrated circuit and the first connector.

6. **(ORIGINAL)** An apparatus as recited in claim 1 wherein the second device is an integrated circuit.

7. **(ORIGINAL)** An apparatus as recited in claim 1 wherein the first device has an inductive coupling coefficient substantially the same as the inductive coupling coefficient of the second device.

8. **(ORIGINAL)** An apparatus as recited in claim 1 wherein the alternating pairs of conductors are reversed once between the first connector and the second connector.

9. **(ORIGINAL)** An apparatus as recited in claim 1 wherein alternating pairs of conductors in the second plurality of conductors are reversed.

10. **(CURRENTLY AMENDED)** An apparatus comprising:

a first integrated circuit including a plurality of differential drivers;

a first connector coupled to the first integrated circuit;

a second connector coupled to the first connector through a plurality of electrical conductors, wherein alternating pairs of the electrical conductors are reversed such that at least one pair of conductors is reversed at a crossover position closer to the first connector than the second connector; and

a second integrated circuit coupled to the second connector, wherein the second integrated circuit includes a plurality of differential receivers.

11. **(ORIGINAL)** An apparatus as recited in claim 10 further comprising a second plurality of electrical conductors coupled between the second connector and the second integrated circuit, wherein alternating pairs of the second plurality of electrical conductors are reversed.

12. **(ORIGINAL)** An apparatus as recited in claim 10 further comprising a second plurality of electrical conductors coupled between the second connector and the second integrated circuit, wherein each pair of conductors includes an inverted conductor and a non-inverted conductor, each inverted conductor coupled to a non-inverted input of one of the differential receivers, and each non-inverted conductor coupled to an inverted input of one of the differential receivers.

13. **(ORIGINAL)** An apparatus as recited in claim 10 wherein the first integrated circuit has an inductive coupling coefficient substantially the same as the inductive coupling coefficient of the second integrated circuit.

14. **(ORIGINAL)** An apparatus as recited in claim 10 wherein the alternating pairs of electrical conductors are reversed once between the first connector and the second connector.

15. **(CURRENTLY AMENDED)** An apparatus comprising:

a printed circuit board;

a plurality of connectors disposed on the printed circuit board;

a first integrated circuit disposed on a first substrate, wherein the first substrate is configured to be coupled to one of the plurality of connectors;

a second integrated circuit disposed on a second substrate, wherein the second substrate is configured to be coupled to one of the plurality of connectors; and

a first plurality of electrical conductors coupled to the plurality of connectors, wherein alternating pairs of conductors between adjacent connectors are reversed such that at least one pair of conductors is reversed at a crossover position closer to one of the plurality of connectors than another of the plurality of connectors.

16. **(ORIGINAL)** An apparatus as recited in claim 15 wherein the printed circuit board is a backplane.

17. **(ORIGINAL)** An apparatus as recited in claim 15 further comprising a second plurality of conductors coupled between the first integrated circuit and one of the plurality of connectors, wherein alternating pairs of conductors have reversed polarity.

18. **(ORIGINAL)** An apparatus as recited in claim 15 wherein the first substrate is a printed circuit board.

19. **(ORIGINAL)** An apparatus as recited in claim 15 wherein the first substrate is a memory module.

20. **(ORIGINAL)** An apparatus as recited in claim 15 wherein the first integrated circuit is a memory device.

21. **(ORIGINAL)** An apparatus as recited in claim 15 wherein the first integrated circuit has an inductive coupling substantially the same as the inductive coupling of the second integrated circuit.

22 - 25. **(CANCELED)**

26. **(CURRENTLY AMENDED)** A method comprising:

generating a plurality of differential signals;

transmitting the plurality of differential signals through a first connector and a second connector to a plurality of differential receivers;

reversing the polarity of alternating differential signals at a crossover position closer to the first connector than the second connector; and

reversing the polarity of alternating differential signals between the second connector and the plurality of differential receivers.

27. **(ORIGINAL)** A method as recited in claim 26 wherein the first connector generated inductive coupling noise as the differential signals are transmitted through the first connector.

28. **(ORIGINAL)** A method as recited in claim 26 wherein the second connector generated inductive coupling noise opposite the noise generated by the first connector as the differential signals are transmitted through the second connector.

29. **(ORIGINAL)** A method as recited in claim 26 further including decoding the plurality of differential signals.

30. **(ORIGINAL)** A method as recited in claim 26 wherein a transmitter package transmits the plurality of differential signals and a receiver package receives the plurality of differential signals.

31. **(ORIGINAL)** A method as recited in claim 30 further including modifying the transmitter package such that the coupling coefficient of the transmitter package is substantially the same as the receiver package.

32. **(CURRENTLY AMENDED)** A method comprising:

modifying a transmitter package such that the coupling coefficient of the transmitter package is substantially the same as the coupling coefficient of a receiver package;

transmitting multiple pairs of differential signals across a plurality of conductors using the transmitter package;

reversing polarity of alternating pairs of differential signal conductors such that at least one pair of conductors is reversed at a crossover position closer to the transmitter package than the receiver package; and

receiving the multiple pairs of differential signals using the receiver package.

33. **(CANCELED)**

34. **(ORIGINAL)** A method as recited in claim 32 further comprising decoding the multiple pairs of differential signals.

35. **(CURRENTLY AMENDED)** A method as recited in claim 32 wherein the differential signals are transmitted through a pair of connectors on [[a]] the plurality of conductors, wherein alternating pairs of conductors are reversed between the pair of connectors.

36 - 38. **(CANCELED)**